In the Specification:

1. Title

Please amend the Title as follows:

Mechanism for Avoiding Multiple Traps <u>After Faulty Access to a Resource</u> ~~Avoidance Mechanism~~

2. Abstract of the Disclosure

The following paragraph reflects the Abstract of the Disclosure as originally filed according to Applicants' records. Based on the Examiner's remarks in the Office Action, it appears that the U.K. priority application was mistakenly copied in the Patent Office file as the examination copy of the application. As noted, the following text reflects the Abstract of the Disclosure as originally filed. If necessary, please amend the PTO file accordingly.

To provide efficient resource access control in a computer system, a trap handler for handling a trap in the event of a faulty resource access being detected is arranged to define a diversion for subsequent access attempts to the same resource. An address translation mechanism is responsive to indication of a diversion for a resource access to modify an address mapping, whereby subsequent attempts to access the resource are diverted in accordance with the diversion. The trap handler can be arranged in a conventional manner to process an exception of the first faulty access to the resource. However, by defining the diversion, which can be used to map further attempts to access the same resource, unnecessary exception processing can be avoided.